Today's plan

- 0. Announcements
- 1. Driving external loads
- 2. Motors: dc motors, stepper motors, servos
- 3. Op-amps
- 4. Comparators

Next time:

- Powering your project
- Measuring capacitance
- midterm solutions

Announcements

• Project Status Report

In your lab: March 7/9/13 (3rd week of project time) I would like a short written status report turned in. The report should discuss your progress so far: what has been accomplished, what remains to be done. If you have encountered problems, discuss them, and your plans to move forward. If you need help to make progress, please mention it.

These reports need not be long, just a few sentences is fine.

Controlling things with the microcontroller

MSP430 P1.x maximum output current: +/- 6 mA (x 3.3V = 20mW)

To drive external loads that are more demanding than logic chips, the MSP430 needs some help.

Some possibilities:

- Beefier logic
- op-amps
- Buffer/driver
- Transistor (bipolar or MOSFET)
- opto-isolators
- Relay
- Solid-state relay
- H Bridge chip (eg for bi-directional motors)

REFERENCE: The Art of Electronics (Horowitz and Hill)

Controlling things with the microcontroller

Driver eg SN75451

up to 300 mA

logic diagram (positive logic)





Driving loads: Transistors



Driving loads: Transistors



Driving loads: Optoisolators



Driving loads: Relays



There are some small low-current relays that can be driven directly by logic chips, again, a diode is essential to protect the logic circuit from the inductive spike on turn-off!

Driving loads: Solid-state Relays

good for AC, large loads, fast, repeated switching (expensive, may need a heat sink), Often will synchronize to line voltage.



eg Crydom D2425: 280VAC, 25A ! \$44

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Be careful buying solid-state relays on the grey (Ebay etc) market, there are many 'fakes' out there.

Motors: DC motors



Motors: Brushless DC motors



http://www.freescale.com/webapp/sps/site/overview.jsp?code=WBT_MOTORBLDCTUT_WP

Motors: Servos





- precise position control
- provide a PWM signal to specify position
- typical range of 0-180 degrees
- contains a DC motor, gearing, a potentiometer, control electronics.
- The average PWM voltage is compared to the position, as measured by the potentiometer. The control electronics then drive the motor forward or backward to set the angle as requested.
- Typical PWM period of 20 ms with on time of $\sim 1 \sim 2$ ms (1.5 ms is 'center'). Wire colours often: red = +5 V, black = ground, white = PWM control.
- Position depends on length of on-pulse, not on duty cycle.

photo from Hobby Servo Fundamentals by Darren Sawicz



Fixed step size, often 200 steps per revolution.

http://www.freescale.com/webapp/sps/site/overview.jsp?code=WBT_MOTORSTEPTUT_WP



From: Introduction to Step Motors, Applied Motion Products. http://www.omega.ca/auto/pdf/REF_IntroStepMotors.pdf

Unipolar vs bipolar windings:





4 wires (usually)

Unipolar stepper (5, 6 or 8 wires)

Unipolar vs bipolar windings:







http://www.haydonkerk.com/Resources/StepperMotorTheory/tabid/192/Default.aspx

Driving Motors: H-bridge

To drive a dc motor in either direction with a single power supply, close S1 and S4 OR S2 and S3.



http://en.wikipedia.org/wiki/File:H_bridge.svg

The switches are often transistors: bipolar or MOSFETs

Driving Motors: H-bridge

SN754410 QUADRUPLE HALF-H DRIVER

SLRS007B - NOVEMBER 1986 - REVISED NOVEMBER 1995

- 1-A Output-Current Capability Per Driver
- Applications Include Half-H and Full-H Solenoid Drivers and Motor Drivers
- Designed for Positive-Supply Applications
- Wide Supply-Voltage Range of 4.5 V to 36 V
- TTL- and CMOS-Compatible High-Impedance Diode-Clamped Inputs
- Separate Input-Logic Supply
- Thermal Shutdown
- Internal ESD Protection
- Input Hysteresis Improves Noise Immunity
- 3-State Outputs
- Minimized Power Dissipation
- Sink/Source Interlock Circuitry Prevents Simultaneous Conduction
- No Output Glitch During Power Up or Power Down
- Improved Functional Replacement for the SGS L293

description

The SN754410 is a quadruple high-current half-H driver designed to provide bidirectional drive currents up to 1 A at voltages from $4.5 \vee to 36 \vee$. The device is designed to drive inductive loads such as relays, solenoids, dc and bipolar stepping motors, as well as other high-current/high-voltage loads in positive-supply applications.



FUNCTION TABLE (each driver)

INPUTST		OUTPUT
Α	EN	Y
н	Н	Н
L	Н	L
х	L	Z

H = high-level, L = low-level X = irrelevant

Z = high-impedance (off)

In the thermal shutdown mode, the output is in a highimpedance state regardless of the input levels.

Driving Motors: H-bridge



Figure 7. Typical Application Schematic

This is for a bipolar stepper motor (or a unipolar stepper ignoring the center taps

Driving Motors: high current op-amp

dual op-amp with 1A output current and thermal protection: TCA0372



Figure 9. Bidirectional DC Motor Control with Microprocessor-Compatible Inputs

Driving Motors: high current op-amp



For circuit stability, ensure that $R_x > \frac{2R3 \cdot R1}{R_M}$ where, R_M = internal resistance of motor. The voltage available at the terminals of the motor is: $V_M = 2(V_1 - \frac{VS}{2}) + |R_0| \cdot I_M$ where, $|R_0| = \frac{2R3 \cdot R1}{R_X}$ and I_M is the motor current.

Figure 10. Bidirectional Speed Control of DC Motors

Op-amps

- Vout = A (V₊-V₋), where A is a large number $(10^{4}-10^{6})$
- Exact value of A is usually not very important.
- (Almost) always use in a circuit with feedback.
- Needs power supplies, often not shown on circuit diagrams. Typical supplies are +/- 15V, but can vary.



Golden Rules

- (1) The output attempts to do whatever is necessary to make the voltage difference between the inputs zero.
- (2) The inputs draw no current.
- Both rules are approximations! Rule (1) is only obeyed in a circuit with negative feedback, and the difference is usually not quite 0. The inputs draw a tiny amount of current.



Inverting amplifier

- $V_{out} = -V_{in} R_f/R_{in}$
- Often the preferred topology, except:
- Low-ish input impedance (R_{in})



Non-inverting amplifier

•
$$V_{out} = V_{in} (1 + R_f/R_g)$$

 input impedance limited by the op-amp itself (usually very high!)



Imperfections

- Gain, A, is not frequency independent. The op-amp gain will roll off at high frequency (design choice for stability).
- inputs do draw some current (input bias current) must provide a dc current path!
- Output cannot swing beyond (or in some cases even too close to) the power supply rails.
- many other small imperfections (input bias voltage, input offset voltage, input offset current etc)

Single supply amplifier

- When working with microcontrollers it is often convenient to have an amplifier that can be powered from 0/5V or 0/3.3V rather than +/-15V.
- Previous circuits need some modifications: (a) need to reference inputs from the supply midpoint. (b) often want to AC couple the input.

TI has some nice documents:

https://courses.cit.cornell.edu/bionb440/datasheets/SingleSupply.pdf

www.ti.com/lit/ml/sloa091/sloa091.pdf

www.ti.com/lit/an/sloa030a/sloa030a.pdf

Somewhat awkward choices:



Figure 3. AC-Coupled Gain Stages

• Easier choice for inverting AC amplifier:



Figure A–22. Inverting AC Amplifier

• Easier choice for inverting AC amplifier: add a capacitor in parallel with R_F to low-pass filter



Figure A-22. Inverting AC Amplifier

• And non-inverting amplifier:



Figure A–23. Noninverting AC Amplifier



Figure A–23. Noninverting AC Amplifier

2nd order filters:



Figure 16. Sallen-Key Low- and High-Pass Filter Topologies

 If you need to generate Vcc/2 (which is the opamp "ground") use a second op-amp:



Basic device function: compare two voltages, indicate which is greater.

But also useful for:

- logic level shifting,
- threshold detection/ generating square waves
- driving the P-channel mosfet or pnp transistor on H-bridges
- turning a logic output into a 'tri-state' output.



 V_{cc} , V_{FE} - +, - supplies. The inputs must stay between the supply voltages. Can be +/-15V or -

When V > V+, then the output is connected to GND. When V+ > V-, the output floats.

Balance: used to trim internal V+ vs V- offsets. Not usually needed. Strobe: pull to ground to disable comparator.

Noisy signals:

http://hades.mech.northwestern.edu/index.php/Comparators

http://hades.mech.northwestern.edu/index.php/Comparators

The pull-up doesn't have to be connected to the same supply voltage as the comparator supply, it can be higher or lower. This makes the comparator output very flexible for level shifting!

Example: Level shifting:

Q3 and Q4 can be turned on with a 5V logic device, off at 0.

But Q1 and Q2 need to be up at 12V to be turned off, then pulled down to turn on.

But Q1 and Q2 need to be up at 12V to be turned off, then pulled down to turn on.