

1

2

3

4

5

6

A

A

B

B

C

C

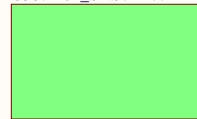
D

D

SH1
C587-201_S1.SchDoc



SH2
C587-201_S2.SchDoc



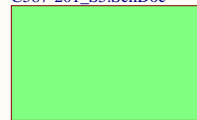
SH3
C587-201_S3.SchDoc



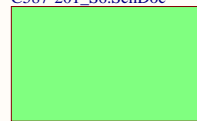
SH4
C587-201_S4.SchDoc



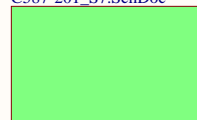
SH5
C587-201_S5.SchDoc



SH6
C587-201_S6.SchDoc



SH7
C587-201_S7.SchDoc



MCE Assembly #: 003161 ASY

Title		<i>MCEv2 5 MDM Instrument Backplane</i>		University of British Columbia	
Size:	Number:	Revision:	Department of Physics & Astronomy		
B	ELE-C587-201	C Issue 0	MCEv2 Project		
Date:	Time:	Sheet	6224 Agricultural Road		
4/10/2018	2:37:11 PM	0 of 5	Vancouver BC V6T 1Z1 Canada		
File:		C587-201_S0.SchDoc			



1

2

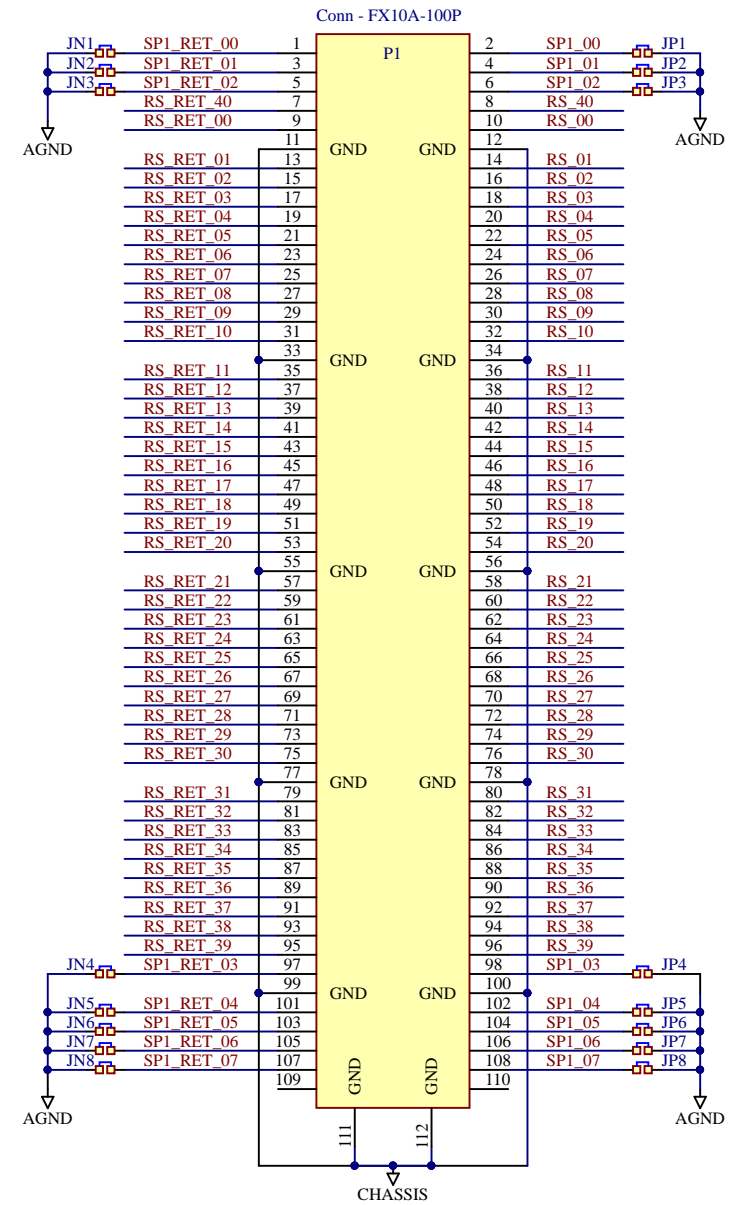
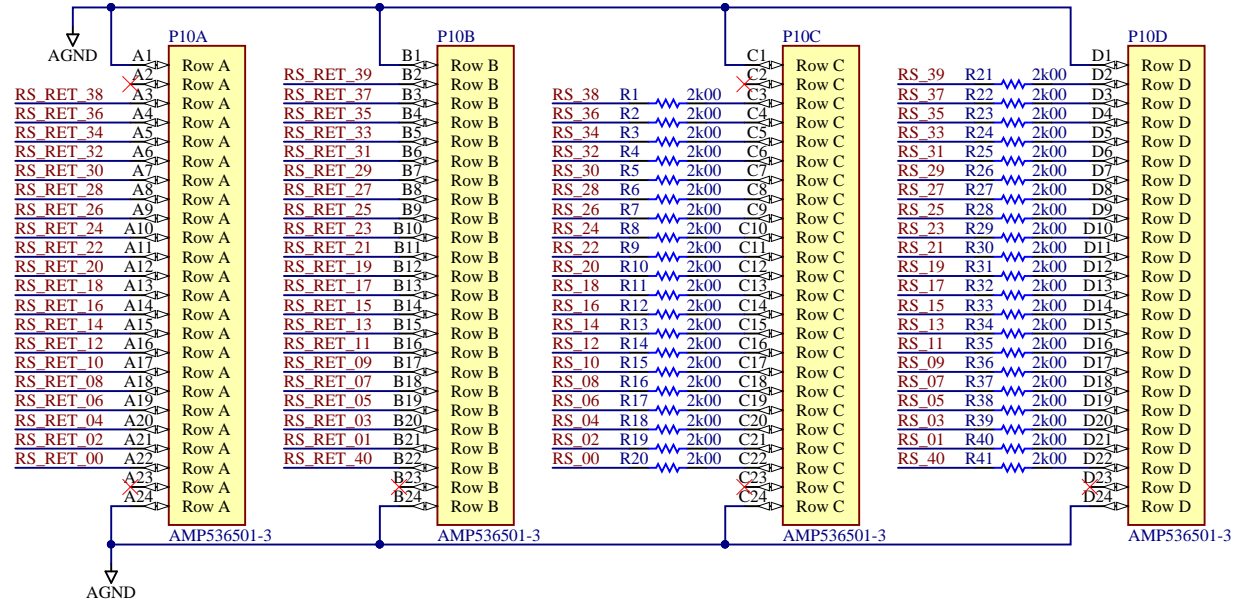
3

4

5


6

Address Card P10

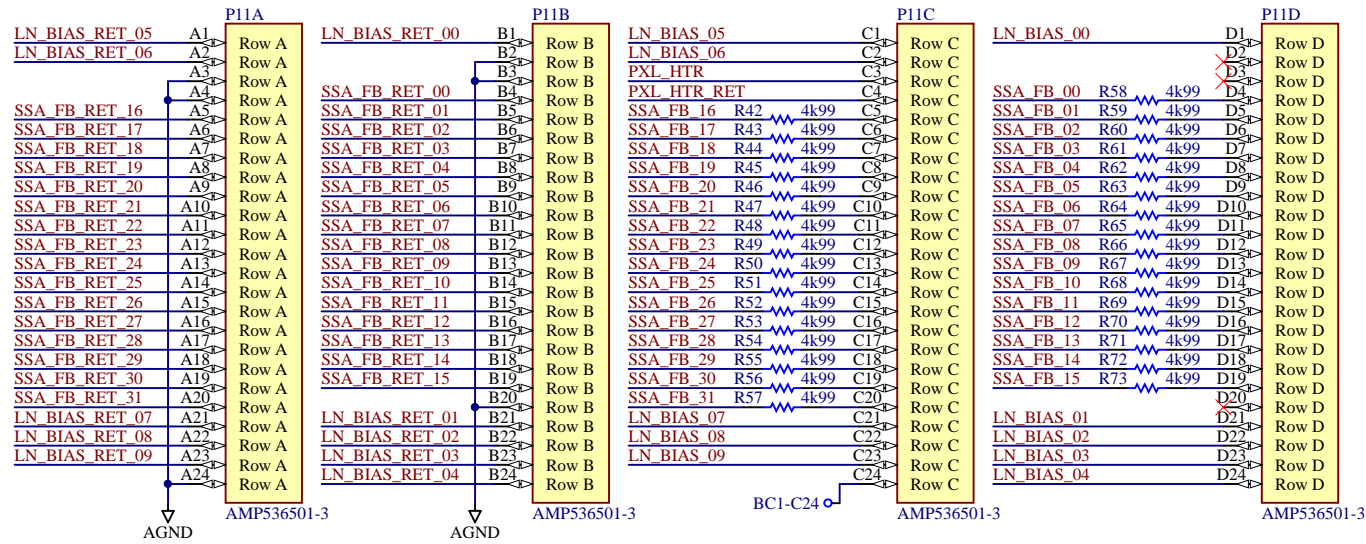


PCB Part Number for BOM: 003163 PCB
MCE Assembly #: 003161 ASY

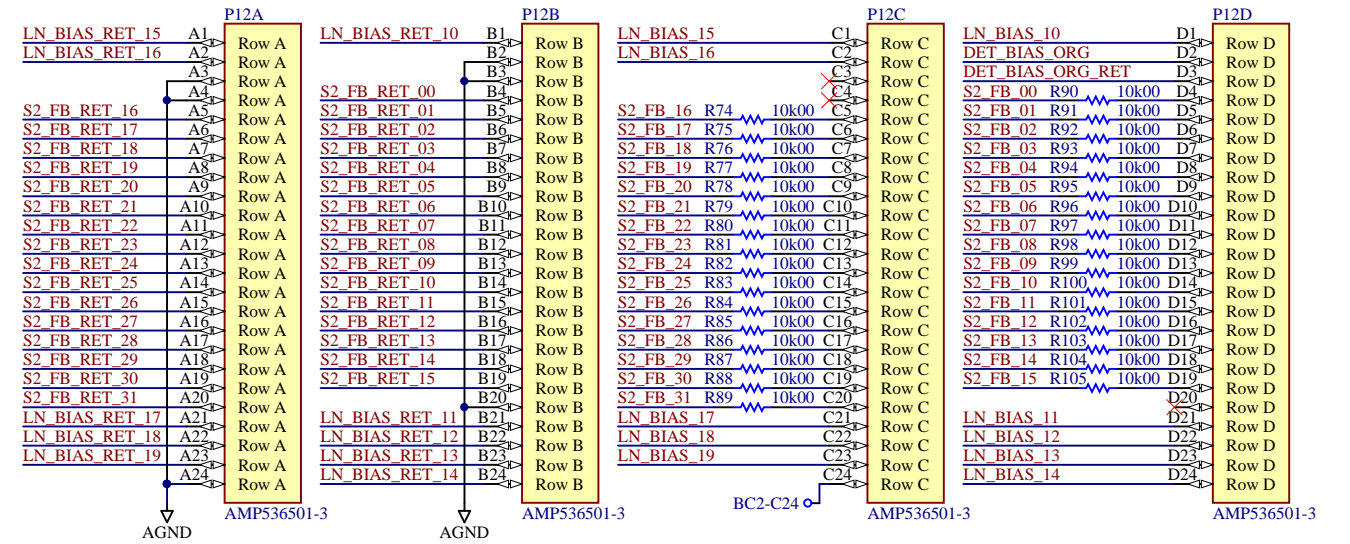
Title		MCEv2 5 MDM Instrument Backplane		University of British Columbia	
Size: B	Number: ELE-C587-201	Revision: C Issue 0	Department of Physics & Astronomy		
Date: 4/10/2018	Time: 2:37:11 PM	Sheet 1 of 7	MCEv2 Project		
File: C587-201_S1.SchDoc			6224 Agricultural Road		
			Vancouver BC V6T 1Z1 Canada		



Bias Card 1 - P11

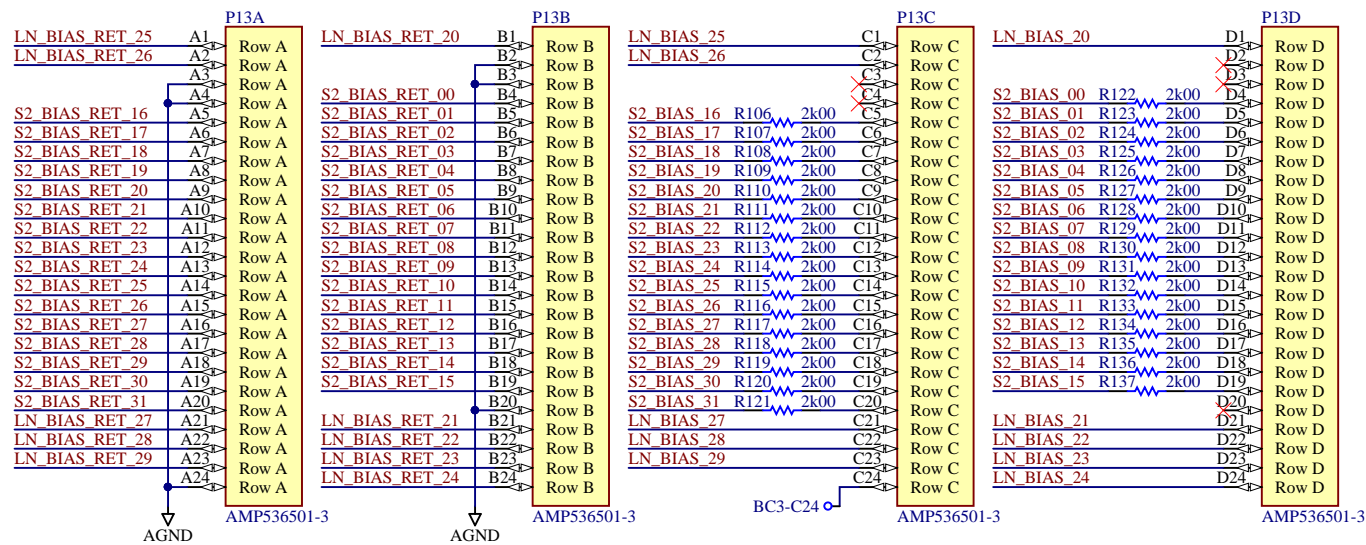


Bias Card 2 - P12

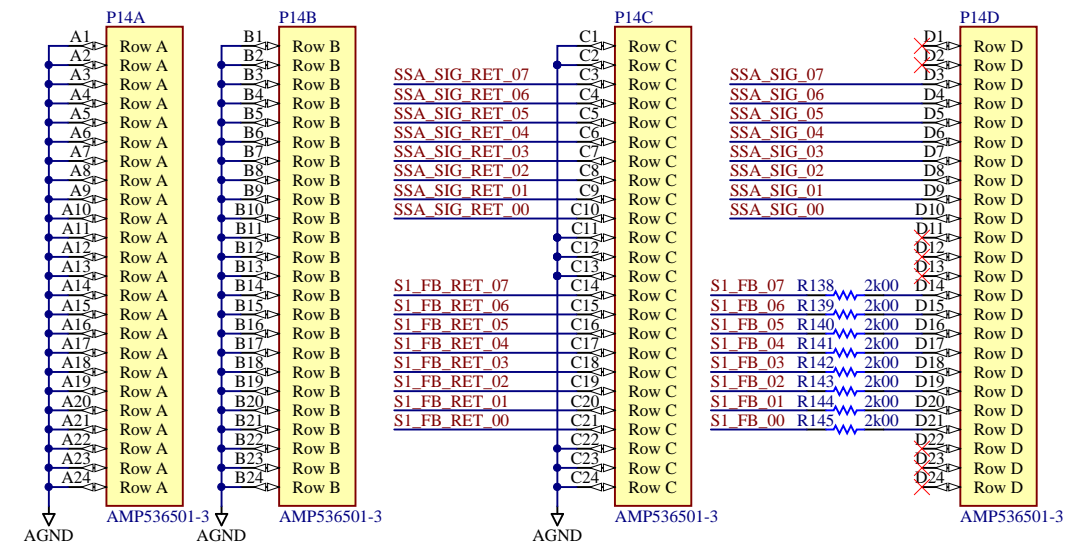


Note 1: BC2 is compatible with Address Cards
 Note 2: Compatibility Requires no Connection at C3, C4 for Bias Card 2 & 3
 Note 3: BC1-BC3 are not compatible with Bias Cards that have SSAM_HTR_A and B parts installed

Bias Card 3 - P13

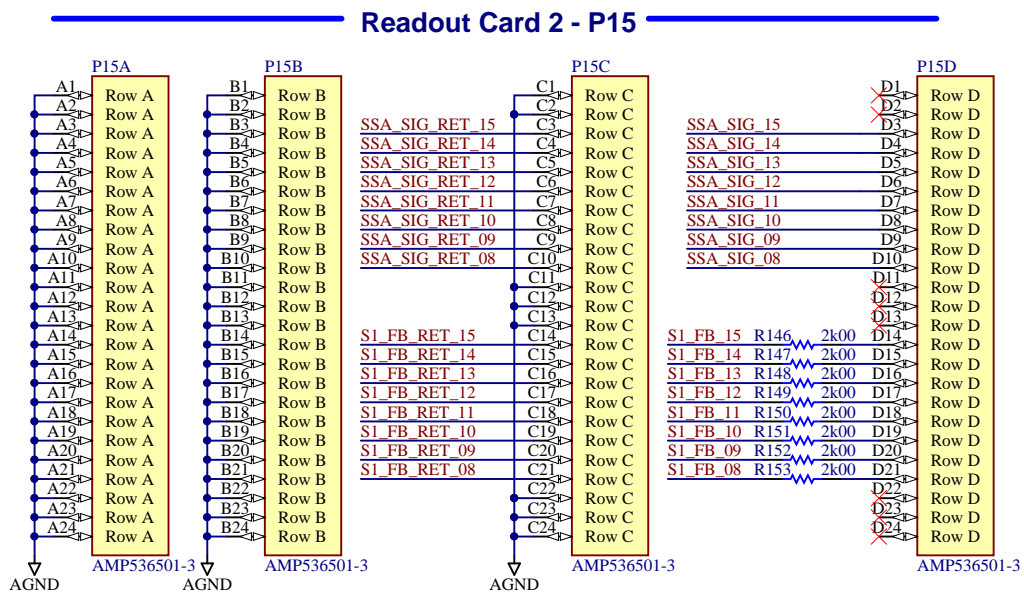


Readout Card 1 - P14

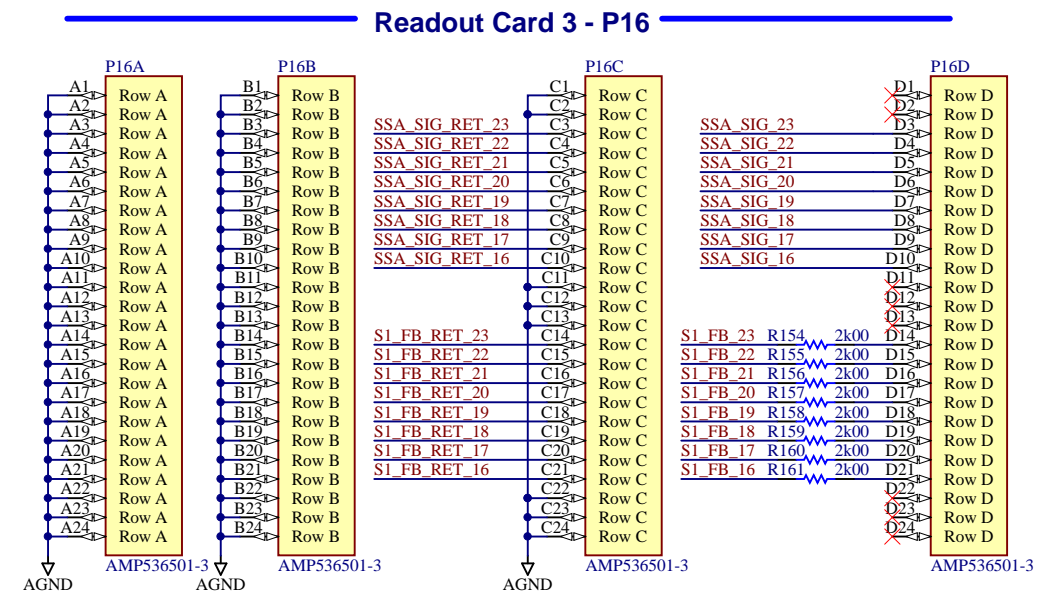


SSA_SIG_x Signals are Outputs from Array (Inputs on ReadoutCards)
 S1_FB_x Signals are Inputs to Array (Outputs on Readout Cards)

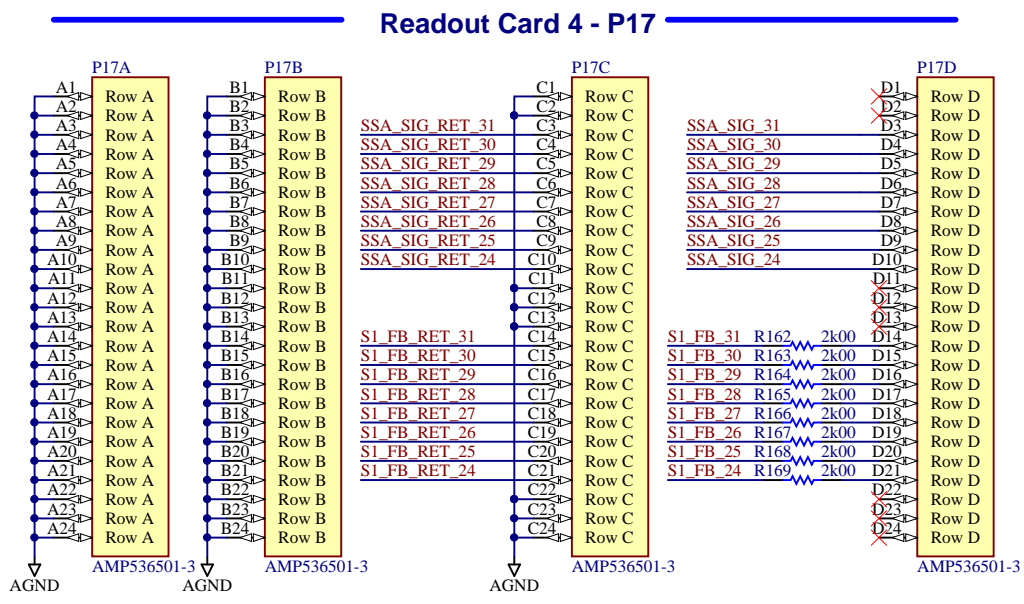
MCE Assembly #: 003161 ASY



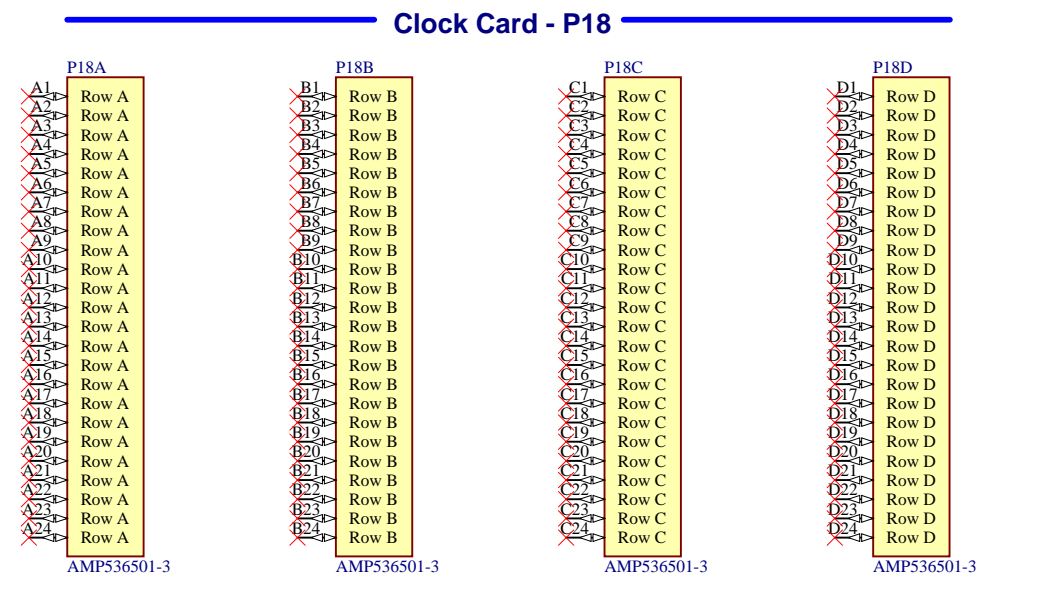
SSA_SIG_x Signals are Outputs from Array (Inputs on ReadoutCards)
 S1_FB_x Signals are Inputs to Array (Outputs on Readout Cards)



SSA_SIG_x Signals are Outputs from Array (Inputs on ReadoutCards)
 S1_FB_x Signals are Inputs to Array (Outputs on Readout Cards)




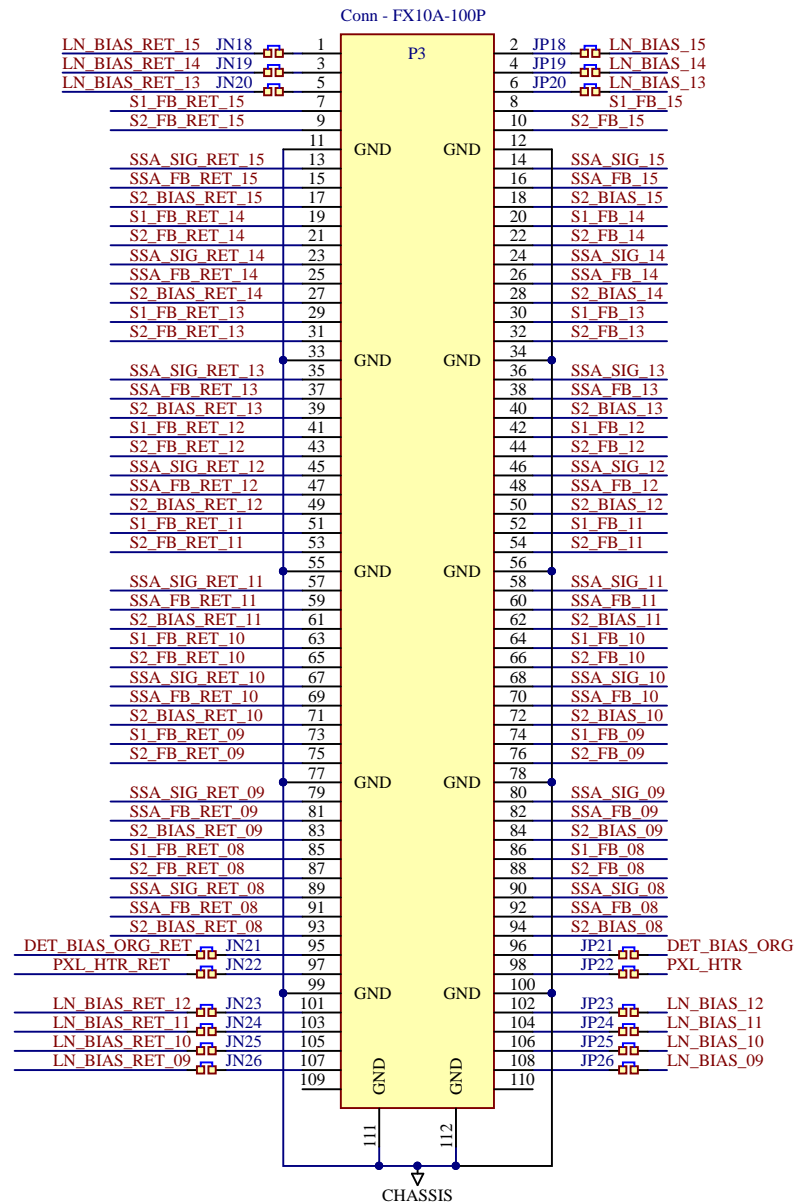
SSA_SIG_x Signals are Outputs from Array (Inputs on ReadoutCards)
 S1_FB_x Signals are Inputs to Array (Outputs on Readout Cards)



MCE Assembly #: 003161 ASY

Title		MCEv2 5 MDM Instrument Backplane		University of British Columbia	
Size: B	Number: ELE-C587-201	Revision: C	Issue 0	Department of Physics & Astronomy	
Date: 4/10/2018	Time: 2:37:12 PM	Sheet 3	of 7	MCEv2 Project	
File: C587-201_S3.SchDoc				6224 Agricultural Road	
				Vancouver BC V6T 1Z1 Canada	

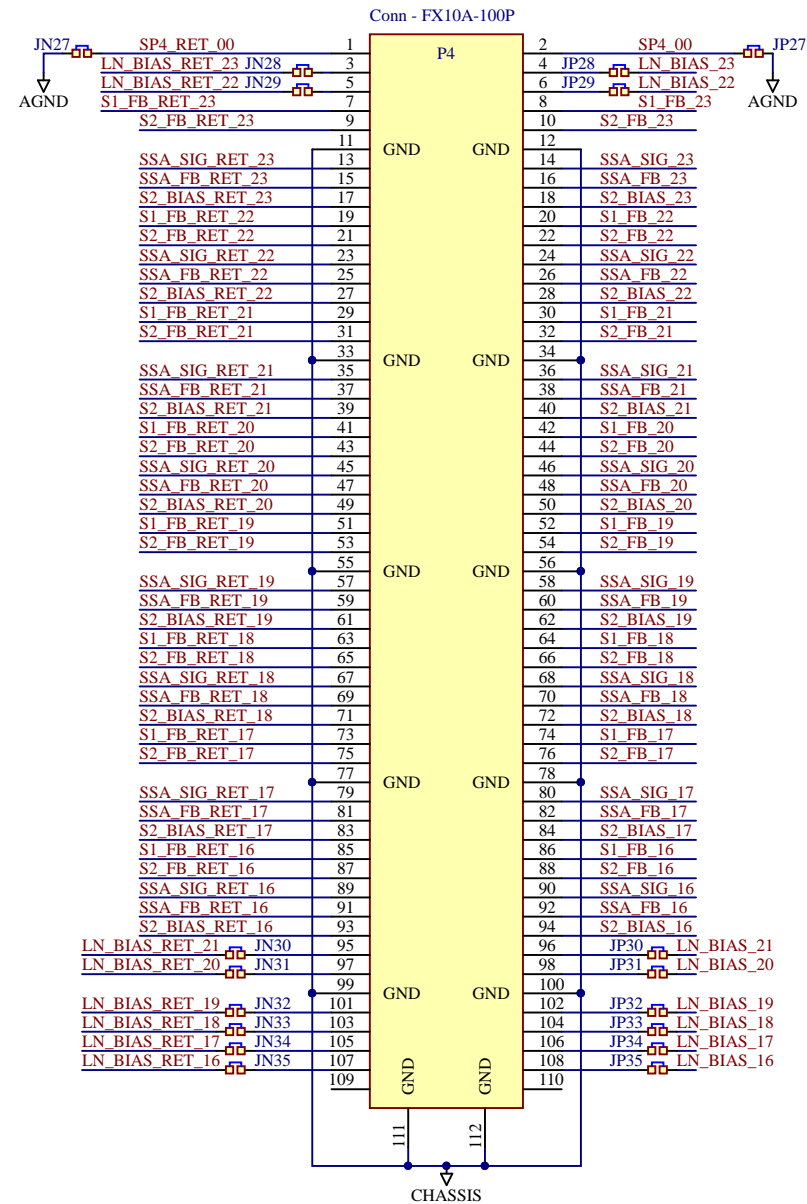




MCE Assembly #: 003161 ASY

Title		MCEv2 5 MDM Instrument Backplane		University of British Columbia	
Size: B	Number: ELE-C587-201	Revision: C Issue 0	Department of Physics & Astronomy		
Date: 4/10/2018	Time: 2:37:13 PM	Sheet 5 of 7	MCEv2 Project		
File: C587-201_S5.SchDoc				6224 Agricultural Road	
				Vancouver BC V6T 1Z1 Canada	

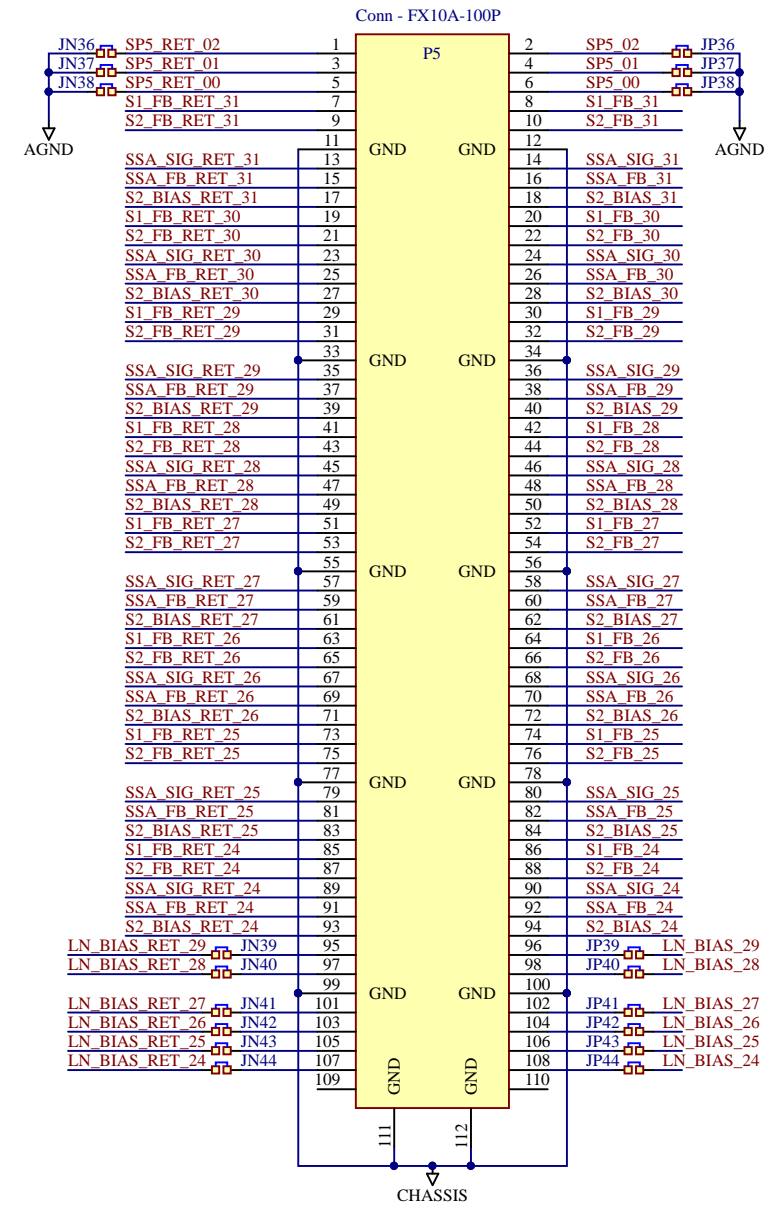




MCE Assembly #: 003161 ASY

Title		MCEv2 5 MDM Instrument Backplane		University of British Columbia	
Size: B	Number: ELE-C587-201	Revision: C	Issue 0	Department of Physics & Astronomy	
Date: 4/10/2018	Time: 2:37:13 PM	Sheet 6	of 7	MCEv2 Project	
File: C587-201_S6.SchDoc				6224 Agricultural Road	
				Vancouver BC V6T 1Z1 Canada	



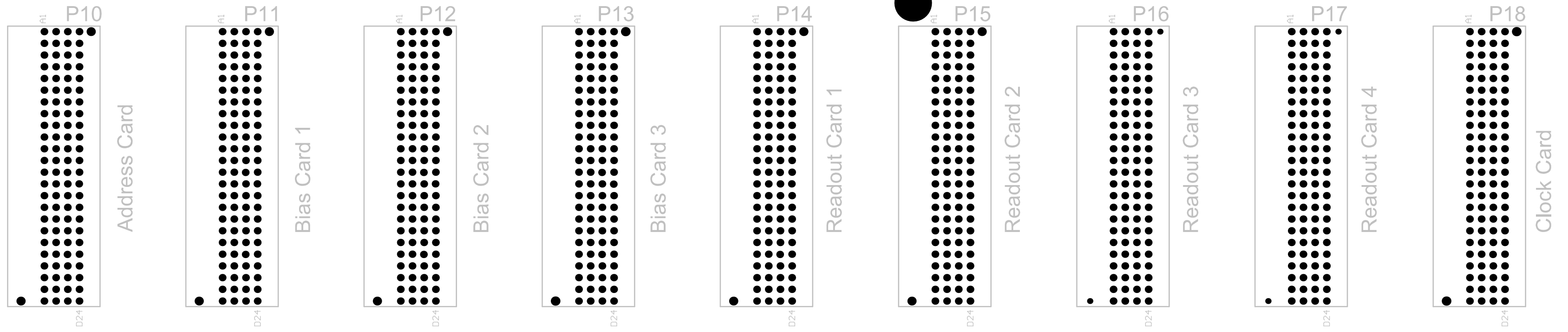


Use UBC Logo
 Add Fiducial Point
 Add Fiducial Point
 MCE Assembly #: 003161 ASY

Title		MCEv2 5 MDM Instrument Backplane		University of British Columbia	
Size: B	Number: ELE-C587-201	Revision: C Issue 0	Department of Physics & Astronomy		
Date: 4/10/2018	Time: 2:37:13 PM	Sheet 7 of 7	MCEv2 Project		
File: C587-201_S7.SchDoc			6224 Agricultural Road		
			Vancouver BC V6T 1Z1 Canada		



MCEv2 - 5 MDM INSTRUMENT BACKPLANE
 University of British Columbia
 Department of Physics & Astronomy - MCE Lab
 ELE-C587-201 Rev C



003162 PCB
 003161 ASY

Layer Stack Up Detail for: C587-201_Inst_BP.PcbDoc

Layer Name
Top Layer
Sig1
Ret1
Sig2
Ret2
Sig3
Ret3
Sig4
Ret4
Bottom Layer

Layer Name: Top Overlay
 C587-201_Inst_BP.PcbDoc
 Filename: Top Assembly Drawing
 4/10/2018 2:37:16 PM



Layer Stack Up Detail for: C587-201_Inst_BP.PcbDoc	
Layer	Material
Top Layer	
Sig1	
Ret1	
Sig2	
Ret2	
Sig3	
Ret3	
Sig4	
Ret4	
Bottom Layer	

