#### Today's plan:

- Reminder: Marking
- Announcements: status report
- Final presentations and reports
- Connecting resistive or current sensors
- Motor drivers
- Op-Amp
- Powering your project
- Analog outputs

#### **Reminder - Marking:**

To pass the course you have to:

1. Submit all three reports and check all the required parts (show them working to the TAs or instructor)

2. Present the final project and submit the final report.

If any of these elements are missing, your grade will be lower of 45% or total of the points.

Marking:

A Lecture test 20%

Activities 5%

Programs and lab reports in first 6 weeks 20%

Project proposal 3%

Status report 2%

Project quality and functionality 20%

Presentation 10%

Final report 20%

Late report submissions - 10% of the grade will be subtracted per day down to 50%. We wave it for good reasons

#### Announcements: Status Report

I would like a short written status report from everyone turned in at start of the lab during the week of March 25.

The report should discuss your progress so far: what has been accomplished, what remains to be done. If you have encountered problems, discuss them, and your plans to move forward. If you need help to make progress, please mention it.

These reports need not be long, just a few sentences or points are fine.

# Announcements: Final Reports and presentations

•At the end of the course you'll present your project in two ways:

1) Oral presentation in class. These will happen on Tuesday April 9, Wednesday April 10 and Thursday April 11. These are reasonably informal. We will all look at the presentation, with slides if any, and the working project.

2) Formal written report, due April 15, 10PM.

Unfortunately no extensions!

## Connecting resistive or current sensors



R the same order of magnitude As the resistance range of the sensor

#### Controlling things with the microcontroller

MSP430 P1.x maximum output current: +/-6 mA (x 3.3V = 20mW)

To drive external loads that are more demanding than logic chips, the MSP430

Some possibilities:

.op-amps
.Buffer/driver
.Transistor (bipolar or MOSFET)
.opto-isolators
.Relay
.Solid-state relay
.H Bridge chip (eg for bi-directional motors)

REFERENCE: The Art of Electronics (Horowitz and Hill)

#### Driving Motors: H-bridge

To drive a dc motor in either direction with a single power supply, close S1 and S4 OR S2 and S3.



The switches are often transistors: bipolar or MOSFETs





### Driving DC Motors: H-bridge



Figure 7. Typical Application Schematic

This is for 2 DC motors

### Driving Step Motors: H-bridge



Figure 7. Typical Application Schematic

This is for a bipolar stepper motor (or a unipolar stepper ignoring the center taps)

#### Controlling things with the microcontroller

Driver eg SN75451

up to 300 mA

logic diagram (positive logic)





#### **Driving loads: Transistors**



### **Driving loads: Transistors**





Inductive loads require that you protect the transistor with a diode!

Many FETs have a diode build in

N-channel mosfet

#### **Driving loads: Optoisolators**



#### **Driving loads: Relays**



There are some small low-current relays that can be driven directly by logic chips, again, a diode is essential to protect the logic circuit from the inductive spike on turn-off!

### Driving loads: Solid-state Relays

good for AC, large loads, fast, repeated switching (expensive, may need a heat sink), Often will synchronize to line voltage.



eg Crydom D2425: 280VAC, 25A !

#### Driving Motors: high current op-amp

dual op-amp with 1A output current and thermal protection: TCA0372



Figure 9. Bidirectional DC Motor Control with Microprocessor-Compatible Inputs

#### Driving Motors: high current op-

#### amp

dual op-amp with fA output current and thermal protection: TCA0372



For circuit stability, ensure that  $R_x > \frac{2R3 \cdot R1}{R_M}$  where,  $R_M$  = internal resistance of motor. The voltage available at the terminals of the motor is:  $V_M = 2(V_1 - \frac{V_S}{2}) + |R_0| \cdot I_M$ where,  $|R_0| = \frac{2R3 \cdot R1}{R_x}$  and  $I_M$  is the motor current.

Figure 10. Bidirectional Speed Control of DC Motors

#### Driving Motors: high current op-amp

Single direction on/off



#### **Op-amps**

•Vout = A (V<sub>+</sub>-V<sub>-</sub>), where A is a large number ( $10^4$ - $10^6$ )

•Exact value of A is usually not very important.

•(Almost) always use in a circuit with feedback.

•Needs power supplies, often not shown on circuit diagrams. Typical supplies are +/- 15V, but can vary.

#### **Golden Rules**



•(1) The output attempts to do whatever is necessary to make the voltage difference between the inputs zero.

(2) The inputs draw no current.

•Both rules are approximations! Rule (1) is only obeyed in a circuit with negative feedback, and the difference is usually not quite 0. The inputs draw a tiny amount of current.

#### Inverting amplifier

$$V_{out} = -V_{in} R_f/R_{in}$$

Often the preferred topology, except:
 Low-ish input impedance (R<sub>in</sub>)



#### Non-inverting amplifier

$$\cdot V_{out} = V_{in} (1 + R_f/R_g)$$

 input impedance limited by the opamp itself (usually very high!)



#### Imperfections

•Gain, A, is not frequency independent. The op-amp gain will roll off at high frequency (design choice for stability).

•inputs do draw some current (input bias current) – must provide a dc current path!

•Output cannot swing beyond (or in some cases even too close to) the power supply rails.

Analog Output: The MSP430F5529 doesn't have a DAC. If you want an adjustable analog output, there are a couple of options:

- Add an external DAC (serial or parallel)
- low-pass filter and PWM output.



#### Analog Output:

## Parallel DAC: eg TLC7528, Dual 8bit multiplying digital to analog converter.

<ul> <li>Easily Interfaced to Microproces</li> </ul>	sors
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On-Chip Data Latches

- Monotonic Over the Entire A/D Conversion Range
- Interchangeable With Analog Devices AD7528 and PMI PM-7528
- Fast Control Signaling for Digital Signal Processor (DSP) Applications Including Interface With TMS320
- Voltage-Mode Operation
- CMOS Technology

KEY PERFORMANCE SPECIFICATIONS		
Resolution	8 bits	
Linearity Error	1/2LSB	
Power Dissipation at VDD = 5V	20mW	
Setting Time at VDD = 5V	100ns	
Propagation Delay Time at VDD = 5V	80ns	

#### description

The TLC7528C, TLC7528E, and TLC7528I are dual, 8-bit, digital-to-analog converters (DACs) designed with separate on-chip data latches and feature exceptionally close DAC-to-DAC matching. Data are transforred to silter of the two DAC TLC7528C, TLC7528E, TLC7528I DUAL 8-BIT MULTIPLYING DIGITAL-TO-ANALOG CONVERTERS SLAS062E - JANUARY 1987 - REVISED NOVEMBER 2008

DW, N OR PW PACKAGE (TOP VIEW)			
AGND ( OUTA ( RFBA ( DGND ( DACA/DACB ( (MSB) DB7 ( DB6 ( DB5 ( DB4 (	1 20 2 19 3 18 4 17 5 16 6 15 7 14 8 13 9 12 10 11	OUTB RFBB REFB WR CS DB0 (LSB) DB1 DB2 DB3	
FI (	N PACKAGE	E	
RFBA	OUTA AGND OUTB		
REFA 4 DGND 5 DACA/DACB 6 (MSB) DB7 7 DB6 8 9	2 1 20 1 10 11 12 1	9 18 REFB 17 V <sub>DD</sub> 16 WR 15 CS 14 DB0 (LSB	

#### DAC: TLC7528



operating sequence

